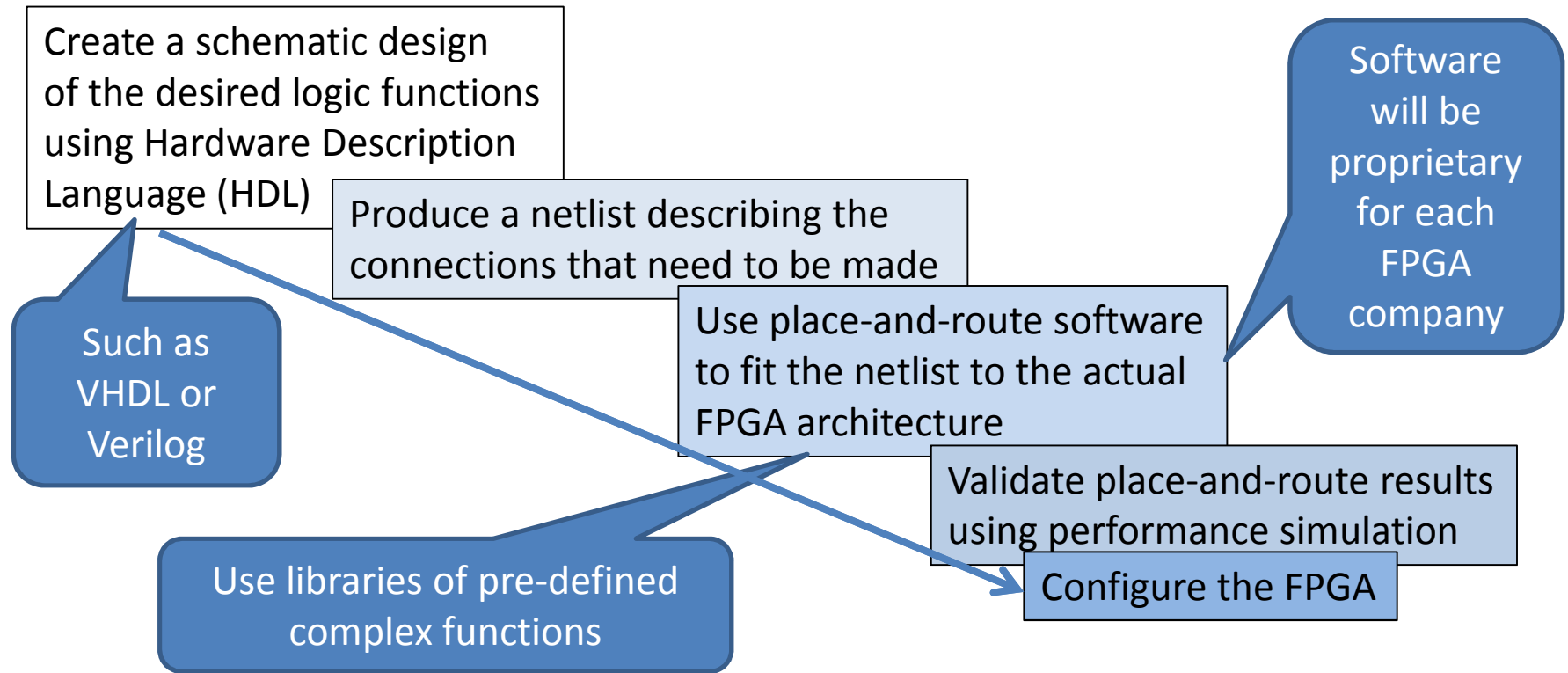


# FPGA DESIGN AND VALIDATION



FPGAs may have memory functions and basic maths functions , or even embedded processors. Validation then gets more difficult due to the large number of possible system states.

The design process is highly software-intensive. Validation of and certification of the FPGA design tools is critical.

Relevant standards include: DO-254, IEEE 7-4.3.2, IEC 61508-2